

L Number	Hits	Search Text	DB	Time stamp
-	1	("6401230").PN.	USPAT	2004/06/24 10:23
-	593	703/13.ccls.	USPAT; US-PGPUB; EPO; JPO	2004/06/24 10:24
-	10	703/13.ccls. and (FIR or finite adj impulse adj response)	USPAT; US-PGPUB; EPO; JPO	2004/06/24 10:32
-	406	716/16.ccls.	USPAT; US-PGPUB; EPO; JPO	2004/06/24 10:33
-	8	716/16.ccls. and (FIR or finite adj impulse adj filter)	USPAT; US-PGPUB; EPO; JPO	2004/06/24 10:40
-	435	716/17.ccls.	USPAT; US-PGPUB; EPO; JPO	2004/06/24 10:40
-	8	716/17.ccls. and (FIR or finite adj impulse adj response)	USPAT; US-PGPUB; EPO; JPO	2004/06/24 10:41
-	5	(716/17.ccls. and (FIR or finite adj impulse adj response)) and coefficient	USPAT; US-PGPUB; EPO; JPO	2004/06/24 10:42
-	279	708/290.ccls.	USPAT; US-PGPUB; EPO; JPO	2004/06/24 10:42
-	38	708/290.ccls. and (FIR or finite adj impulse adj response)	USPAT; US-PGPUB; EPO; JPO	2004/06/24 10:42
-	23	(708/290.ccls. and (FIR or finite adj impulse adj response)) and coefficient	USPAT; US-PGPUB; EPO; JPO	2004/06/24 10:52
-	459	708/300.ccls.	USPAT; US-PGPUB; EPO; JPO	2004/06/24 10:52
-	200	708/300.ccls. and (FIR or finite adj impulse adj response)	USPAT; US-PGPUB; EPO; JPO	2004/06/24 10:53
-	64	(708/300.ccls. and (FIR or finite adj impulse adj response)) and (decimat\$3 or interpol\$5)	USPAT	2004/06/24 10:54
-	1	((708/300.ccls. and (FIR or finite adj impulse adj response)) and (decimat\$3 or interpol\$5)) and coefficient) and (pld or programmable adj logic adj device or FPGA or field adj programmable adj gate adj array)	USPAT	2004/06/24 10:56
-	51	((708/300.ccls. and (FIR or finite adj impulse adj response)) and (decimat\$3 or interpol\$5)) and coefficient	USPAT	2004/06/24 10:57
-	41	((708/300.ccls. and (FIR or finite adj impulse adj response)) and (decimat\$3 or interpol\$5)) and coefficient) and (design or simulat\$3)	USPAT	2004/06/24 11:27
-	264	708/819.ccls.	USPAT	2004/06/24 11:27
-	42	708/819.ccls. and (FIR or finite adj impulse adj response)	USPAT	2004/06/24 11:28
-	0	(708/819.ccls. and (FIR or finite adj impulse adj response)) and (programmable adj device or PLD or FPGA or field adj programmable adj gate adj array)	USPAT	2004/06/24 11:29
-	968	326/38.ccls.	USPAT	2004/06/24 11:30
-	7	326/38.ccls. and (FIR or finite adj impulse adj response)	USPAT	2004/06/24 11:32
-	987	326/39.ccls.	USPAT	2004/06/24 11:32
-	15	326/39.ccls. and (FIR or finite adj impulse adj response)	USPAT	2004/06/24 11:33
-	7	(326/39.ccls. and (FIR or finite adj impulse adj response)) and (PLD or programmable adj logic adj device)	USPAT	2004/06/24 11:35
-	77	706/22.ccls.	USPAT	2004/06/24 11:35

-	15	706/22.ccls. and (FIR or finite adj impulse adj response)	USPAT	2004/06/24 11:38
-	18216	FIR or (finite adj impulse adj response)	USPAT; US-PGPUB; EPO; JPO	2004/06/24 11:40
-	576	(FIR or (finite adj impulse adj response)) and (PLD or programmable adj logic adj device or FPGA or field adj programmable adj gate adj array)	USPAT; US-PGPUB; EPO; JPO	2004/06/24 11:42
-	261	((FIR or (finite adj impulse adj response)) and (PLD or programmable adj logic adj device or FPGA or field adj programmable adj gate adj array)) and (coefficient and response)	USPAT; US-PGPUB; EPO; JPO	2004/06/24 11:43
-	257	((((FIR or (finite adj impulse adj response)) and (PLD or programmable adj logic adj device or FPGA or field adj programmable adj gate adj array)) and (coefficient and response)) and implement\$6	USPAT; US-PGPUB; EPO; JPO	2004/06/24 11:43
-	163	(((((FIR or (finite adj impulse adj response)) and (PLD or programmable adj logic adj device or FPGA or field adj programmable adj gate adj array)) and (coefficient and response)) and implement\$6) and cost	USPAT; US-PGPUB; EPO; JPO	2004/06/24 11:44
-	137	((((((FIR or (finite adj impulse adj response)) and (PLD or programmable adj logic adj device or FPGA or field adj programmable adj gate adj array)) and (coefficient and response)) and implement\$6) and cost) and (serial or parallel)	USPAT; US-PGPUB; EPO; JPO	2004/06/24 11:44
-	70	((((((((FIR or (finite adj impulse adj response)) and (PLD or programmable adj logic adj device or FPGA or field adj programmable adj gate adj array)) and (coefficient and response)) and implement\$6) and cost) and (serial or parallel)) and spectral	USPAT; US-PGPUB; EPO; JPO	2004/06/24 11:45
-	18	(((((((((FIR or (finite adj impulse adj response)) and (PLD or programmable adj logic adj device or FPGA or field adj programmable adj gate adj array)) and (coefficient and response)) and implement\$6) and cost) and (serial or parallel)) and spectral) and (interpolating or decimating)	USPAT; US-PGPUB; EPO; JPO	2004/06/24 12:15
-	1	6634009.pn.	USPAT; US-PGPUB; EPO; JPO	2004/06/24 12:15
-	7637	(digital adj signal adj processing or DSP) and simulat\$3	USPAT; US-PGPUB; EPO; JPO	2004/06/24 14:55
-	6789	((digital adj signal adj processing or DSP) and simulat\$3) and (programmable logic device or PLD or FPGA or field adj programmable adj gate adj array or reprogrammable)	USPAT; US-PGPUB; EPO; JPO	2004/06/24 15:07
-	1980	((digital adj signal adj processing or DSP) and simulat\$3) and (programmable logic device or PLD or FPGA or field adj programmable adj gate adj array or reprogrammable)) and coefficient	USPAT; US-PGPUB; EPO; JPO	2004/06/24 14:57
-	736	(((((digital adj signal adj processing or DSP) and simulat\$3) and (programmable logic device or PLD or FPGA or field adj programmable adj gate adj array or reprogrammable)) and coefficient) and spectral	USPAT; US-PGPUB; EPO; JPO	2004/06/24 14:57

-	680	(((((digital adj signal adj processing or DSP) and simulat\$3) and (programmable logic device or PLD or FPGA or field adj programmable adj gate adj array or reprogrammable)) and coefficient) and spectral) and response	USPAT; US-PGPUB; EPO; JPO	2004/06/24 14:58
-	261	(((((digital adj signal adj processing or DSP) and simulat\$3) and (programmable logic device or PLD or FPGA or field adj programmable adj gate adj array or reprogrammable)) and coefficient) and spectral) and response) and (FIR or finite adj impulse)	USPAT; US-PGPUB; EPO; JPO	2004/06/24 14:59
-	99	(((((digital adj signal adj processing or DSP) and simulat\$3) and (programmable logic device or PLD or FPGA or field adj programmable adj gate adj array or reprogrammable)) and coefficient) and spectral) and response) and (FIR or finite adj impulse)) and file	USPAT; US-PGPUB; EPO; JPO	2004/06/24 14:59
-	908	((digital adj signal adj processing or DSP) and simulat\$3) and (programmable adj logic adj device or PLD or FPGA or field adj programmable adj gate adj array or reprogrammable)	USPAT; US-PGPUB; EPO; JPO	2004/06/24 15:08
-	124	((digital adj signal adj processing or DSP) and simulat\$3) and (programmable adj logic adj device or PLD or FPGA or field adj programmable adj gate adj array or reprogrammable)) and (FIR or finite adj impulse adj response)	USPAT; US-PGPUB; EPO; JPO	2004/06/24 15:08
-	124	(((((digital adj signal adj processing or DSP) and simulat\$3) and (programmable adj logic adj device or PLD or FPGA or field adj programmable adj gate adj array or reprogrammable)) and (FIR or finite adj impulse adj response)) and simulat\$3	USPAT; US-PGPUB; EPO; JPO	2004/06/24 15:09
-	74	(((((digital adj signal adj processing or DSP) and simulat\$3) and (programmable adj logic adj device or PLD or FPGA or field adj programmable adj gate adj array or reprogrammable)) and (FIR or finite adj impulse adj response)) and simulat\$3) and coefficient	USPAT; US-PGPUB; EPO; JPO	2004/06/24 15:09
-	37	(((((digital adj signal adj processing or DSP) and simulat\$3) and (programmable adj logic adj device or PLD or FPGA or field adj programmable adj gate adj array or reprogrammable)) and (FIR or finite adj impulse adj response)) and simulat\$3) and coefficient) and spectral	USPAT; US-PGPUB; EPO; JPO	2004/06/24 15:09
-	1053	702/17,124,189.ccls.	USPAT; US-PGPUB; EPO; JPO	2004/06/28 08:00
-	38	702/17,124,189.ccls. and (FIR or finite adj impulse adj response)	USPAT; US-PGPUB; EPO; JPO	2004/06/28 08:00